

Electronic Acknowledgement Receipt

EFS ID:	7838182
Application Number:	10512017
International Application Number:	
Confirmation Number:	8467
Title of Invention:	SEMICONDUCTOR COMPONENT WITH INTEGRATED CAPACITANCE STRUCTURE AND METHOD FOR FABRICATION THEREOF
First Named Inventor/Applicant Name:	Thomas Benetik
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Filer Authorized By:	Edward Machado
Attorney Docket Number:	10808/169
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		10808-169IssueFeePayment. PDF	155150 f53891ab445e365eb41a3cf1ce9e58cc9c39d395	yes	2

Multipart Description/PDF files in .zip description

Document Description	Start	End
Miscellaneous Incoming Letter	1	1
Issue Fee Payment (PTO-85B)	2	2

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	32437 29aad7bcb8effbb146a743e214ea2a426a1fee21	no	2
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Warnings:

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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